

ABSTRACT OF THE DISCLOSURE

A substrate bias generator which makes device characteristics stable by supplying a predetermined negative voltage to a substrate and minimally reduces current consumption during a self refresh mode. The substrate bias generator comprises a substrate voltage level detector for inputting a substrate voltage and outputting a signal which drives an oscillator in response to the input level, and a controller for inputting a chip active enable signal, a self refresh mode enable signal and an output signal of the substrate voltage level detector and controlling a switching operation of the substrate voltage level detector in response to the input level.

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